

Fig. 1

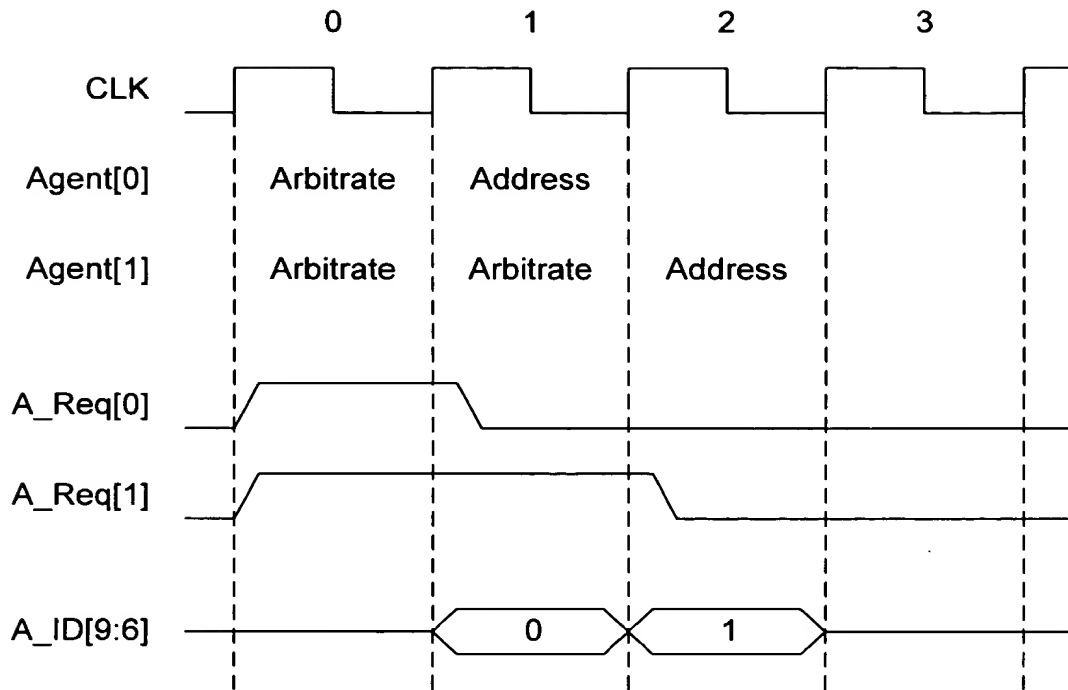


Fig. 2

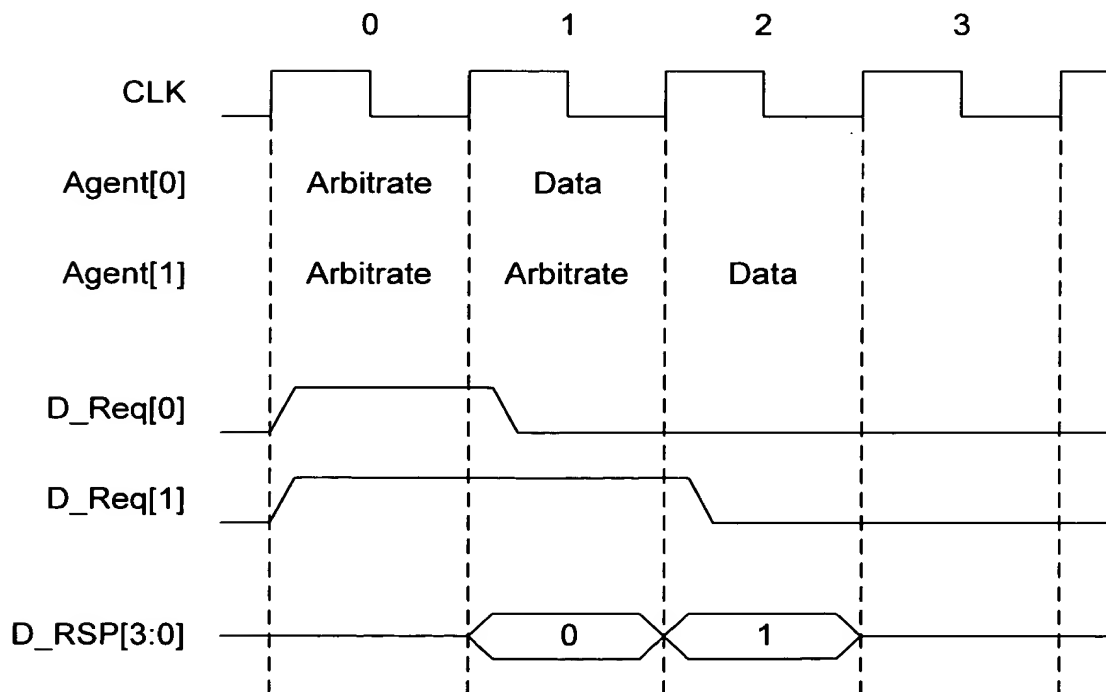


Fig. 3

to Address Drive Logic

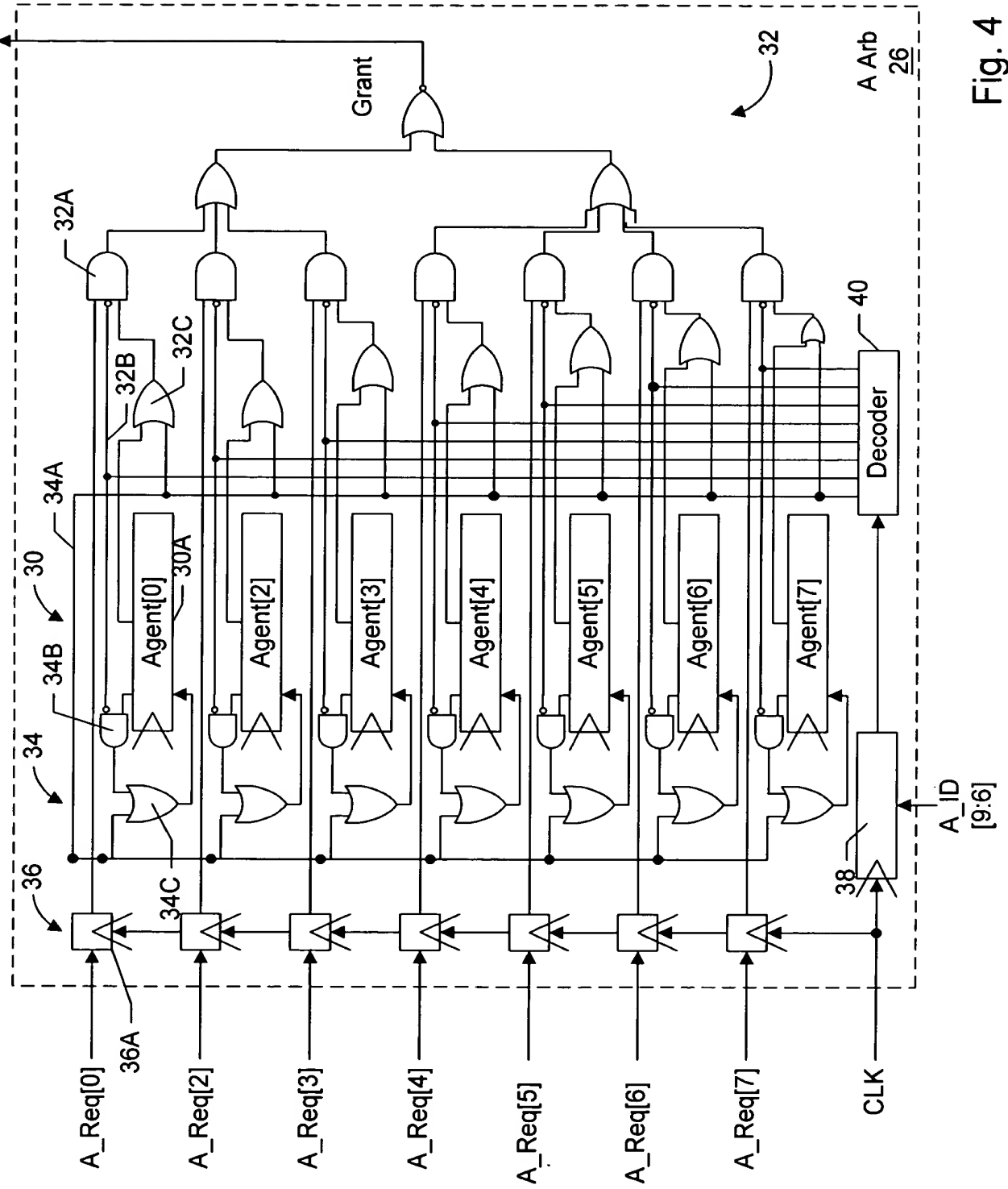
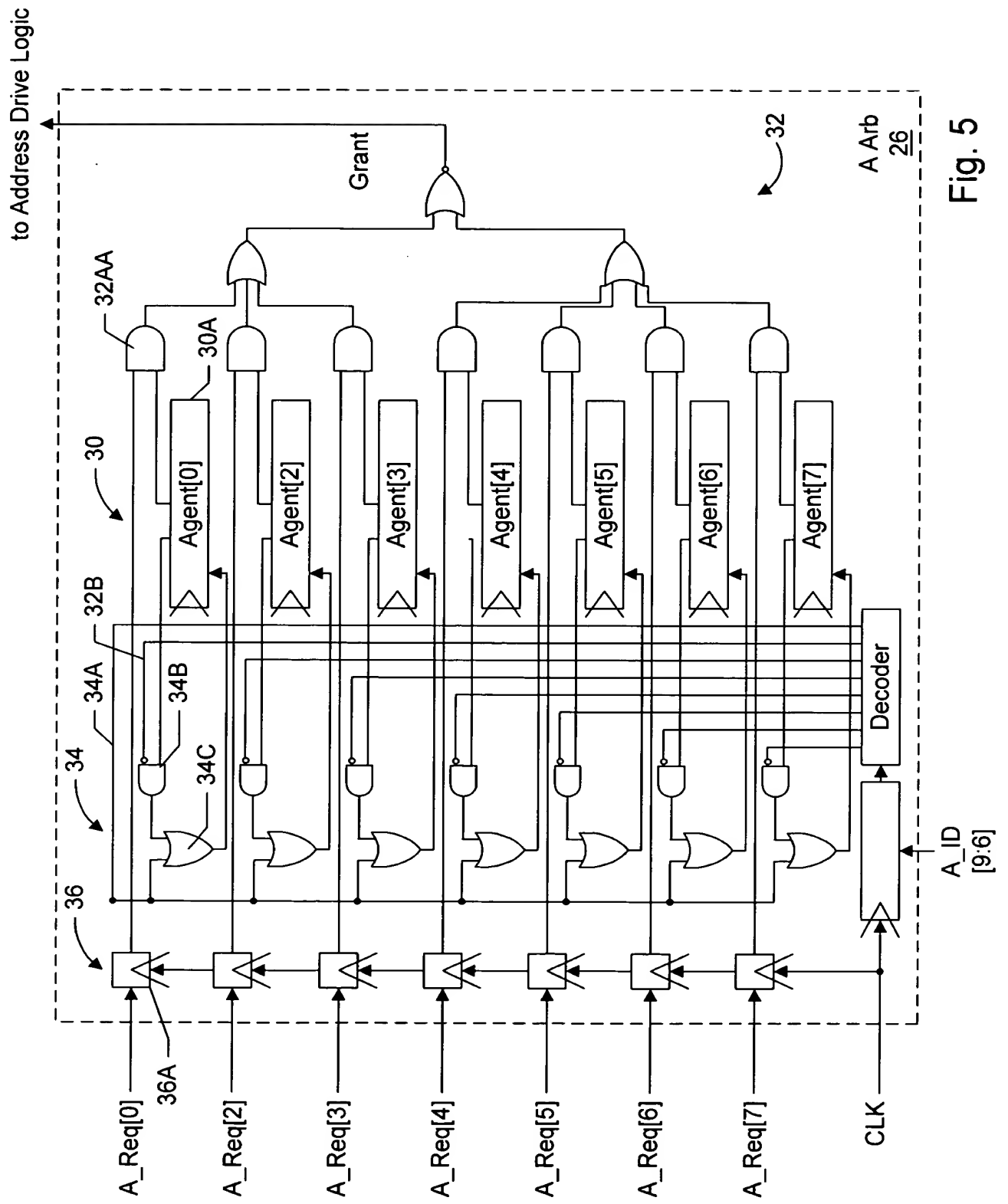


Fig. 4



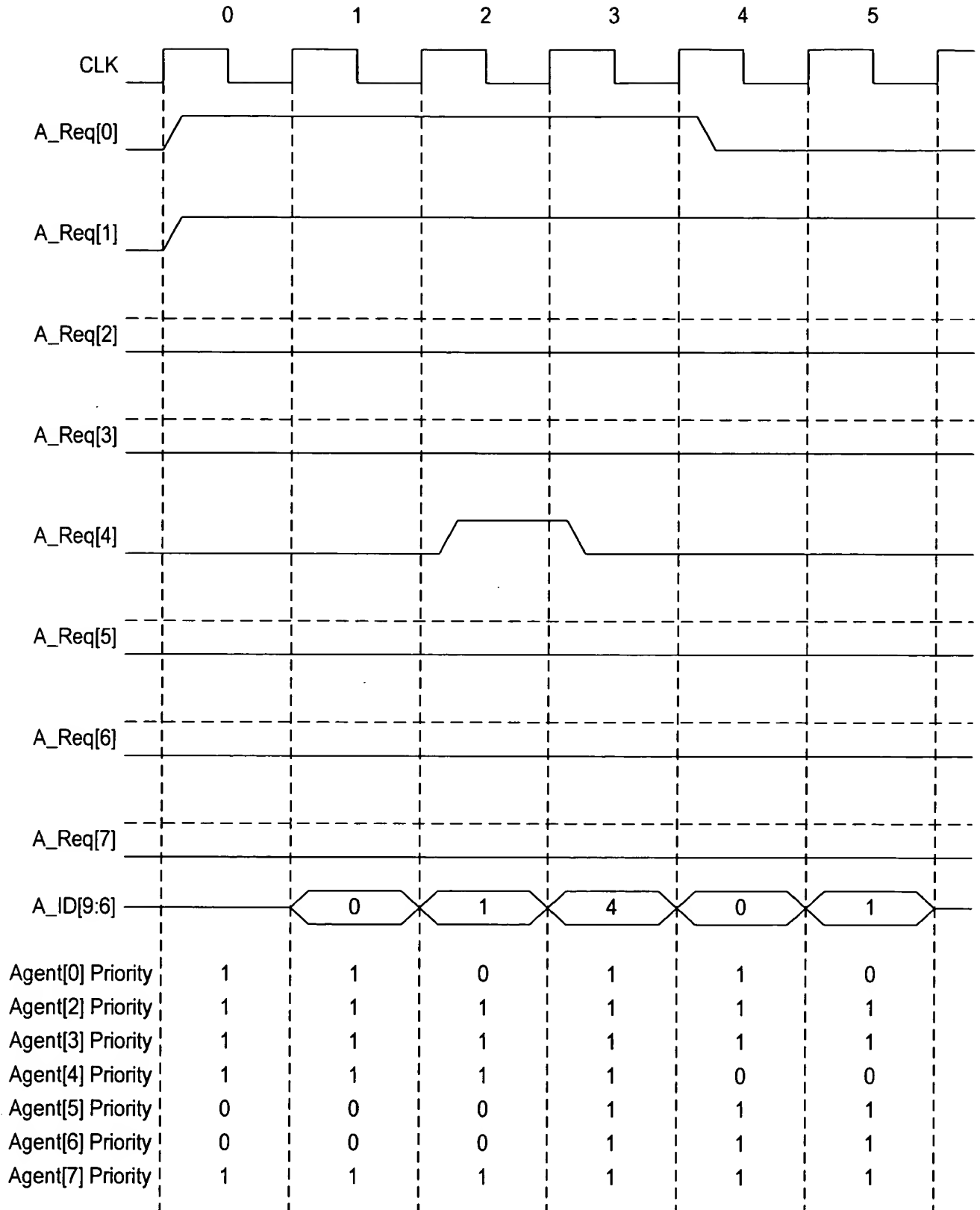


Fig. 6

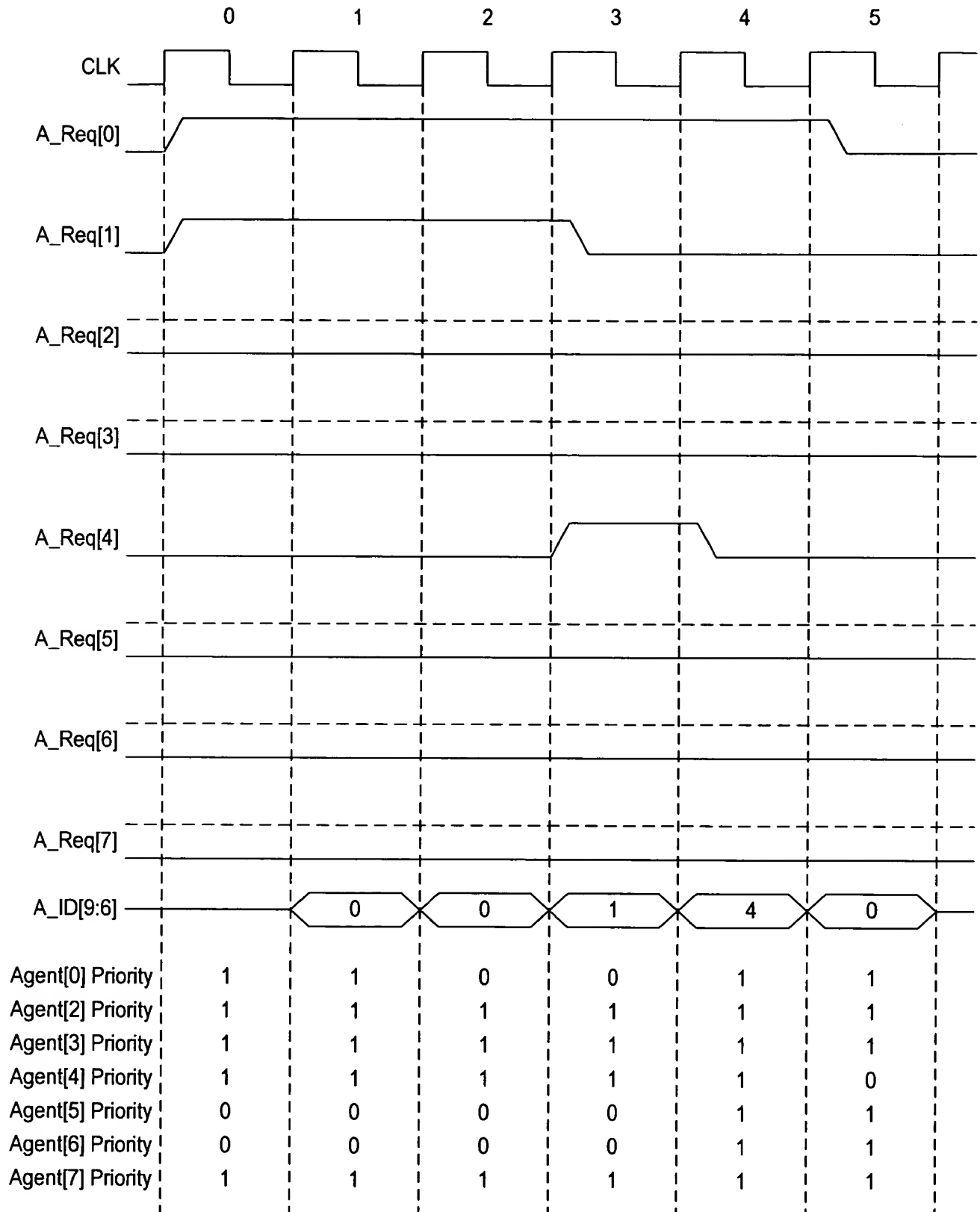


Fig. 7

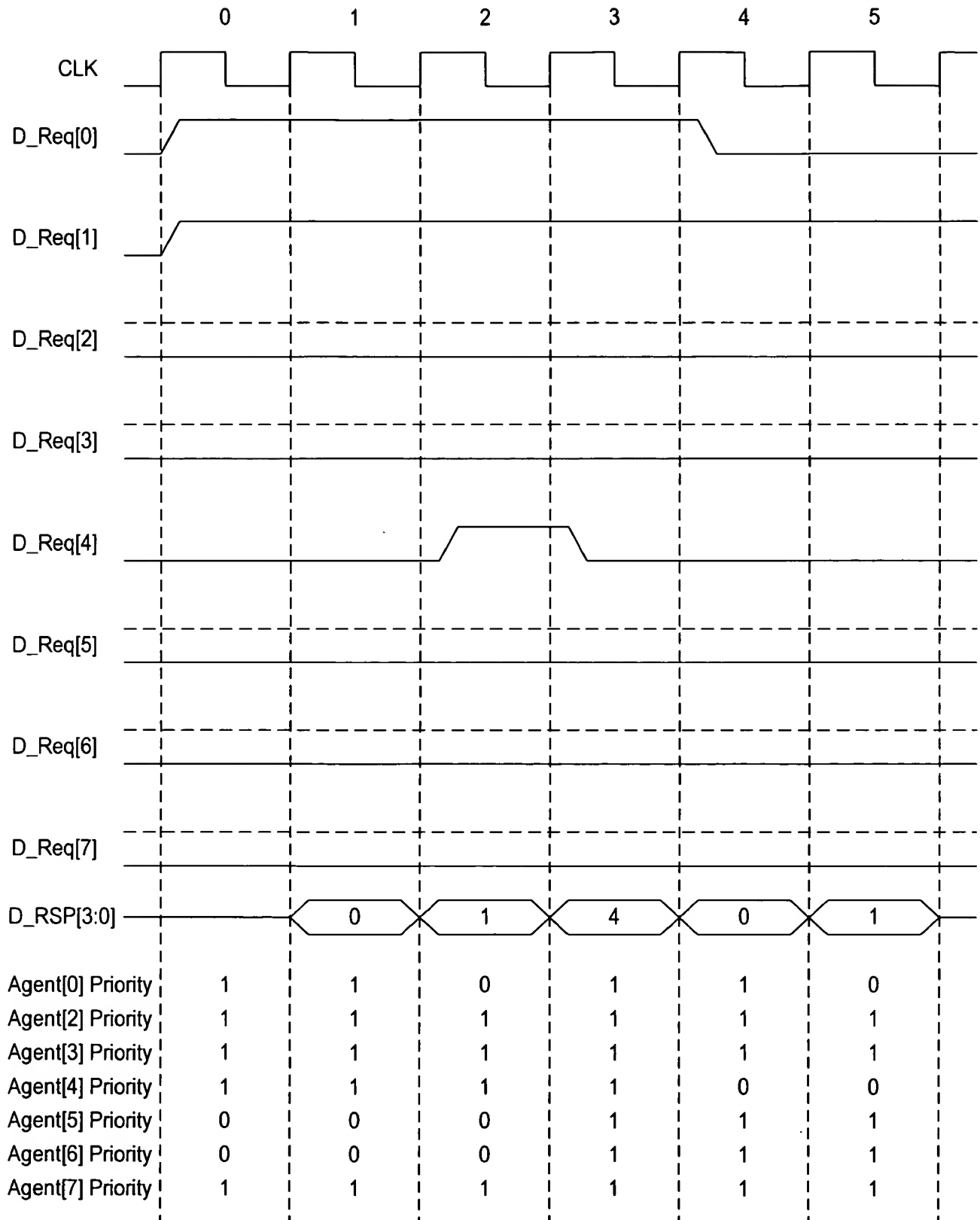
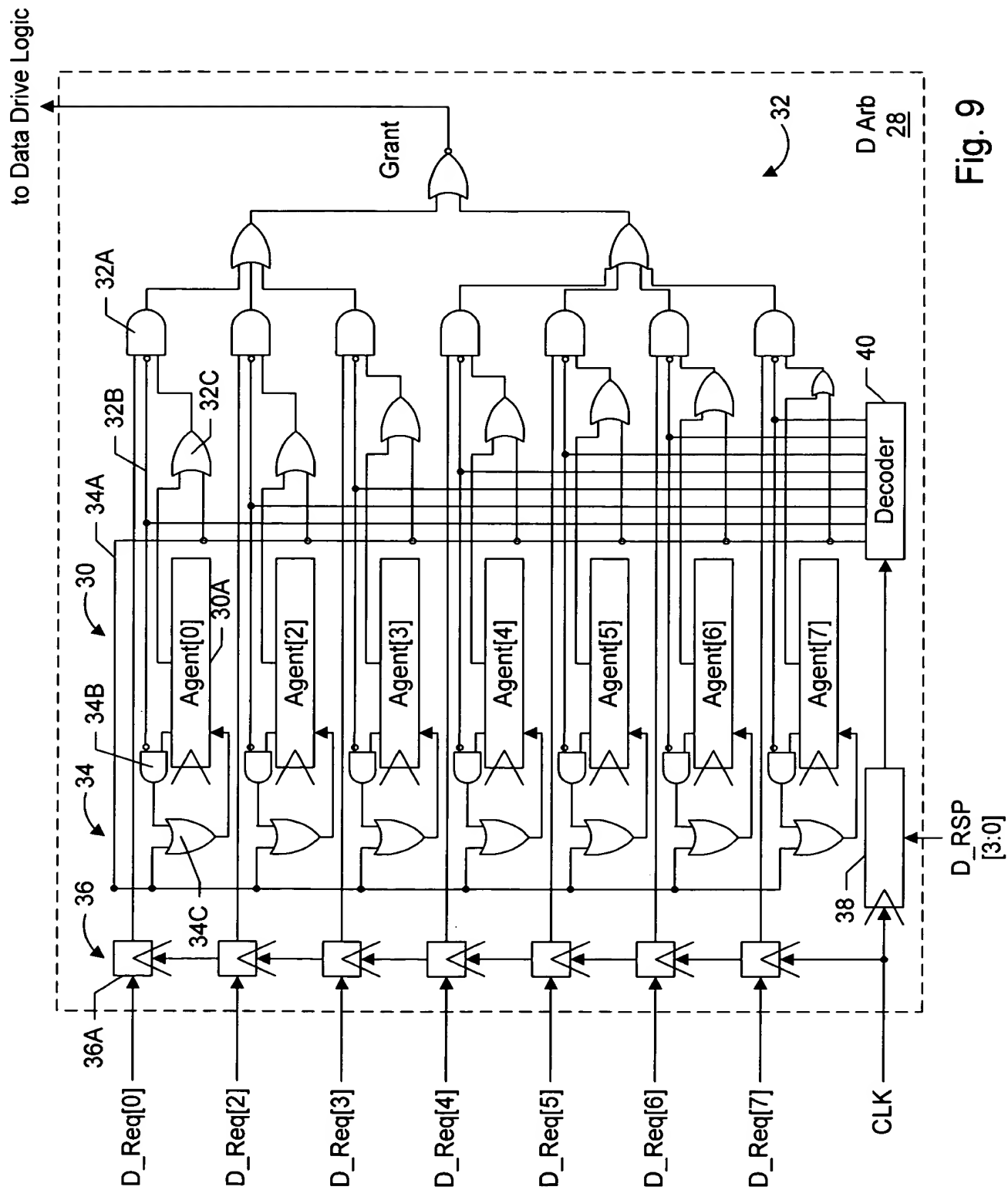


Fig. 8





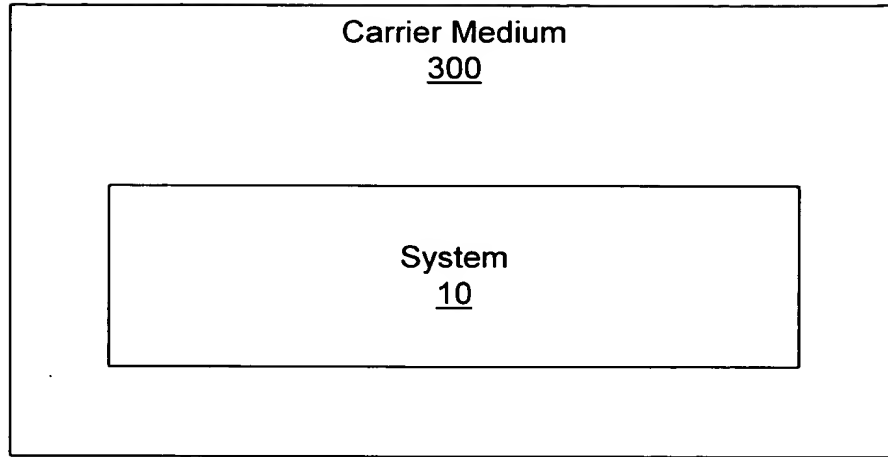


Fig. 10

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